

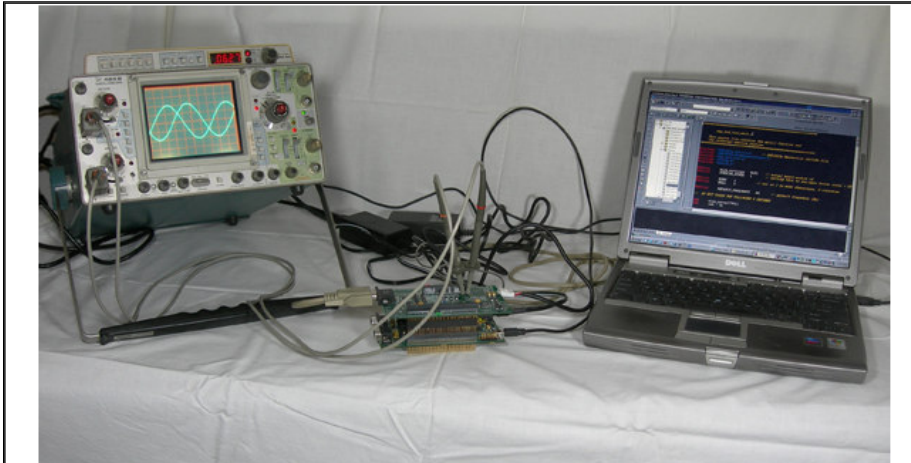
Precision multi-channel data acquisition daughtercard for the F2812 and F28335 eZdsp development systems

The Link Research models LR-F28335DAQ and LR-F2812DAQ daughtercards mate with the F28335 and F2812 eZdsp boards respectively. The daughtercards provide 4 (optionally 8) A/D channels and 8 D/A channels. All A/D channels feature simultaneous sampling, while all D/A channels provide for simultaneous updating. The analog I/O voltage range for all A/Ds and D/As is +/- 10V.

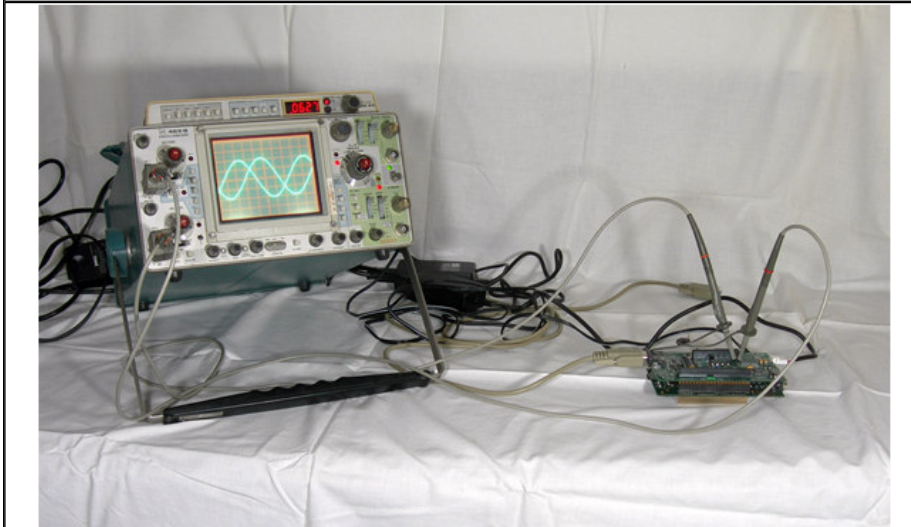
The standard board includes a high-speed serial port, 2 user LEDs and 8 digital inputs. Available options include an Ethernet interface, a fiber optic serial port, and an LCD interface (F2812 version only).

Also available is an additional stackable daughtercard, which provides a total of 16 A/D and 16 D/A channels. Details are available on the Link Research website.

Below are images showing our data acquisition daughtercards in use.



The image above shows a typical use of the model LR-F28335DAQ data acquisition daughtercard (middle). Two of the eight available D/A channels have been programmed to output two +/- 10 Volt sinewaves which are 120 degrees out of phase. On the right is a PC running TI's Code Composer Studio (CCS). Notice the DB-9 cable plugged into the left side of the daughtercard. The other end of this cable is plugged into the PC, where HyperTerminal can be run simultaneously with CCS to monitor or control the F28335 eZdsp.



The image above shows a typical laboratory setup with the model LR-F2812DAQ data acquisition daughtercard. The board is shown plugged into the F2812 eZdsp development system. A test program has been programmed into the eZdsp's FLASH memory, allowing the two board stackup to run stand-alone (i.e. without Code Composer Studio). The software generates eight +/- 10 Volt sinewaves, two of which are monitored with the oscilloscope. The two sinewaves shown are 120 degrees out of phase.